

RESPONSE under 37 C.F.R. § 1.111  
U.S. Appln. No. 10/027,978

**AMENDMENT TO THE SPECIFICATION**

Please REPLACE the abstract with the following amended abstract:

Briefly, in accordance with one embodiment of the invention, a bus controller is adapted to provide virtual-to-physical address translation and memory access protection functions. An apparatus may include a memory controller and a table walk device connected to the memory controller. A translation lookaside buffer (TLB) may be coupled to the table walk device.